



REV A January 2011

Oscilent Controlled Document

Ordering Code / Part Number	Product Description
821-IF70.0M-07B	70.0 MHz IF SAW Filter 7.62 MHz Bandwidth

Specification Contents

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- o Test Circuit
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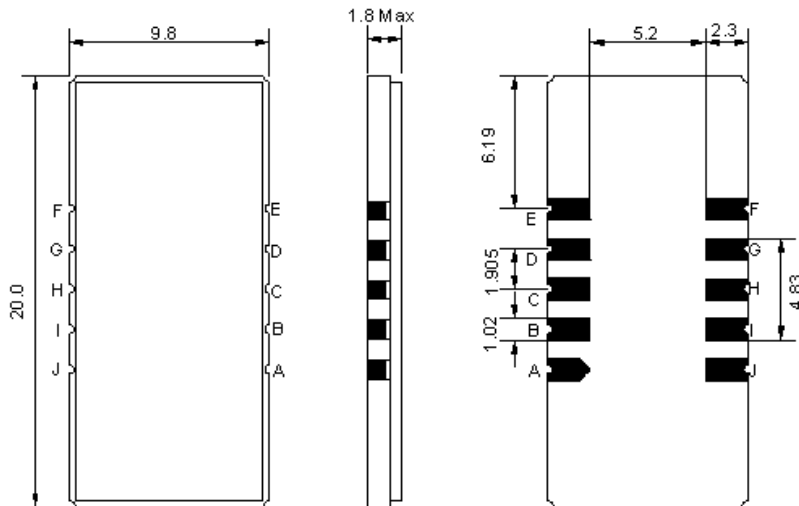
Notes

- o Electrostatic Sensitive Device (ESD) 
- o Avoid excessive ultrasonic exposure
- o Solderability compatible with JEDEC J-STD-020C Pb-free process, 260°C peak reflow temperature
- o This product complies with EU directive 2002/95/EC (RoHS compliance)



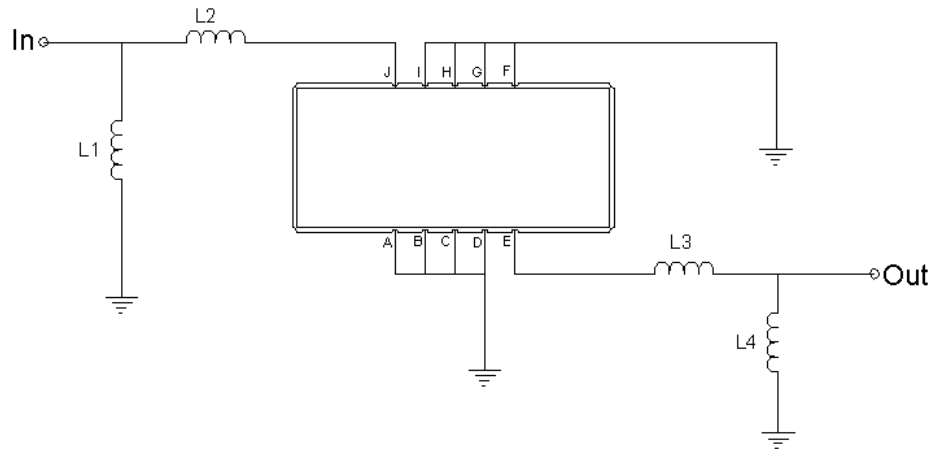


Mechanical Dimensions (mm)



Pin Description	
A, B, C, D, F, G, H, I, J, L	Ground
K	Input
E	Output

Test Circuit



Test Fixture & Values	
Input	L1 = 82 nH, L2 = 82 nH
Output	L3 = 68 nH, L4 = 100 nH
Source/Load Impedance	50 Ω

**Maximum Ratings**

Parameters Description	Unit	Minimum	Typical	Maximum
Operating Temperature Range	°C	-	25	-
Storage Temperature Range	°C	-40	-	85
Maximum DC Voltage	V	-	-	10
Maximum Input Power	dBm	-	-	10
Source Impedance (single ended) ⁽¹⁾	Ω	-	50	-
Load Impedance (single ended) ⁽¹⁾	Ω	-	50	-

Notes: With Matching Network (Ref. Testing Environment Circuit as shown above).

Those impedances could be modified with different impedance values and/or structures, if necessary.

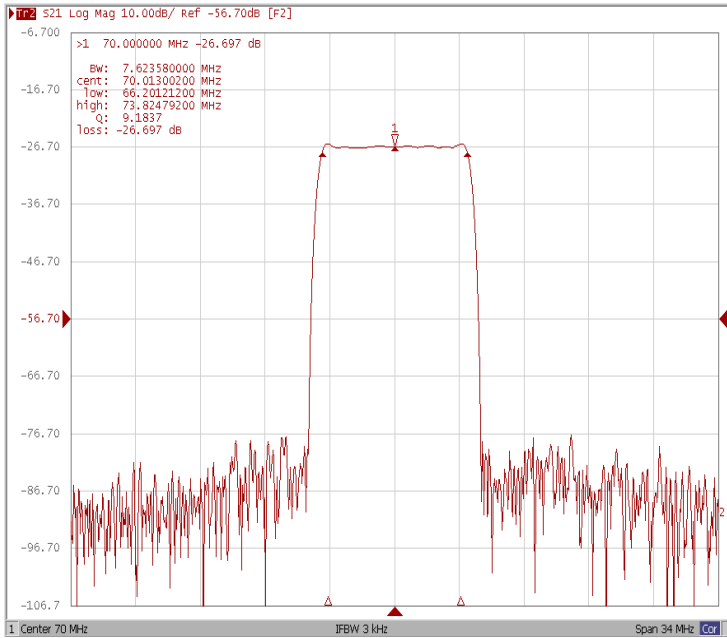
Electrical Specification

Parameters Description	Unit	Minimum	Typical	Maximum
Center Frequency (Fo)	MHz	-	70.0	-
Insertion Loss at Fo	dB	-	26.6	28.5
Group Delay Variation	nsec	-	40	100
Absolute Delay at Fo	usec	-	3.18	-
Passband Ripple Variation	dB	-	0.75	1.0
Bandwidth at -1dB	MHz	7.50	7.62	-
Bandwidth at -10dB	MHz	-	8.26	8.32
Bandwidth at -20dB	MHz	-	8.57	-
Bandwidth at -40dB	MHz	-	8.90	-
Ultimate Rejection	dB	45	50	-
Temperature Coefficient	ppm/°C	-	-20	-

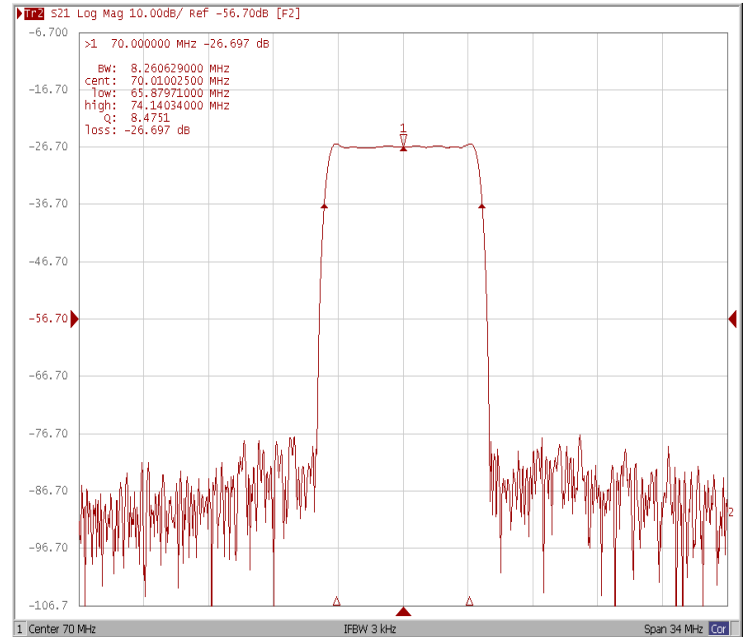


Frequency Response

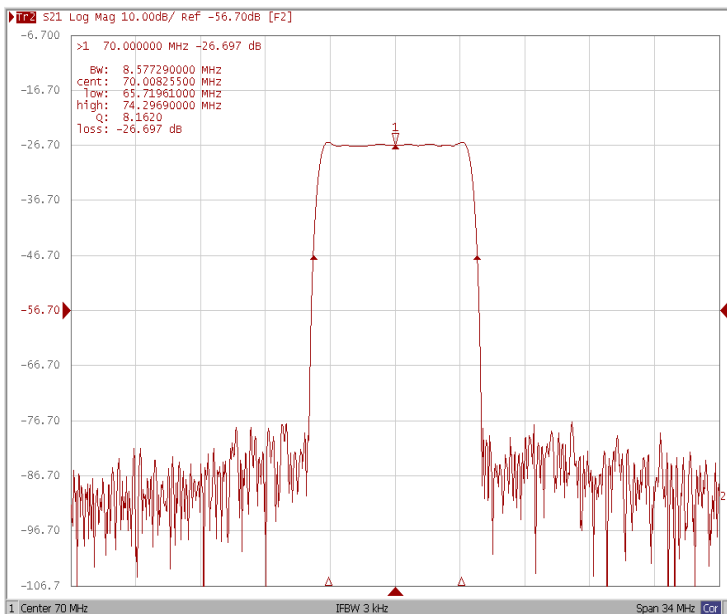
Bandwidth at -1.0 dB



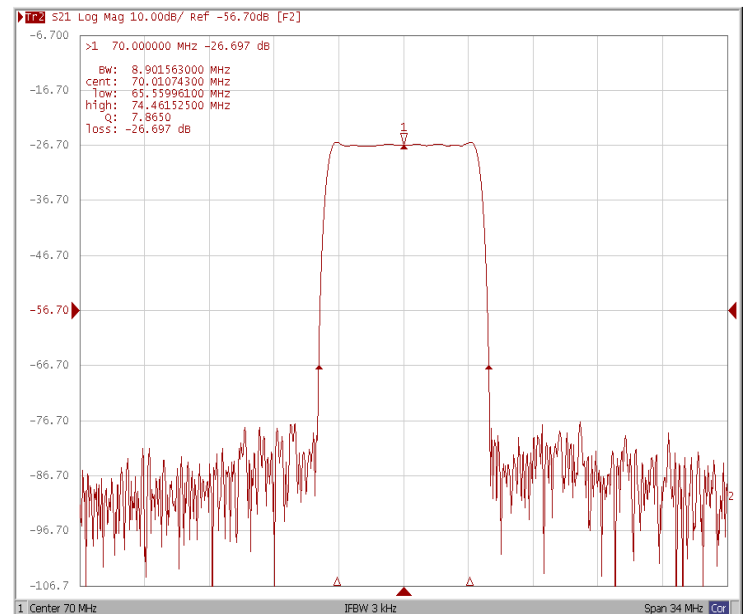
Bandwidth at -10.0 dB



Bandwidth at -20.0 dB

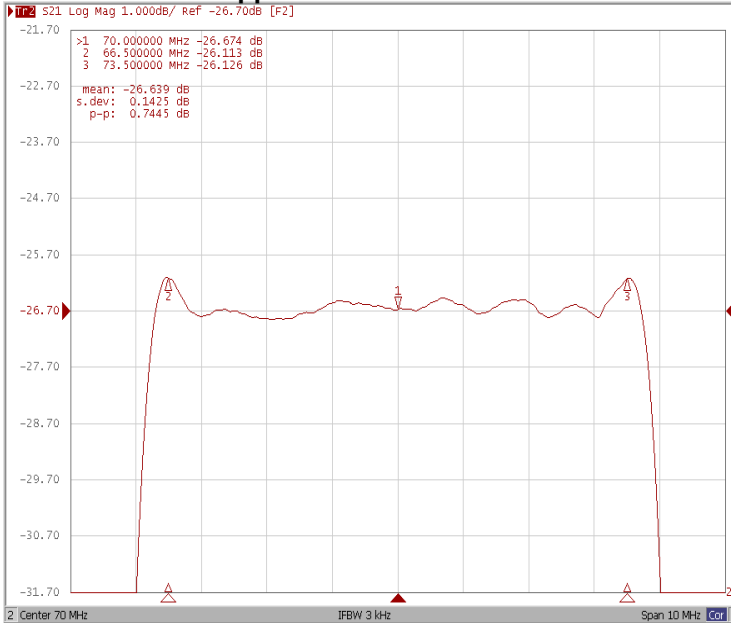


Bandwidth at -40.0 dB

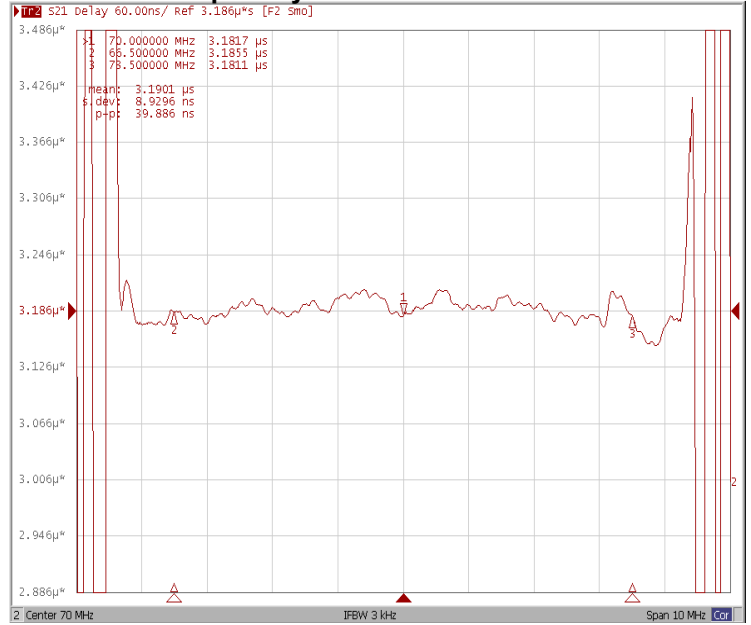




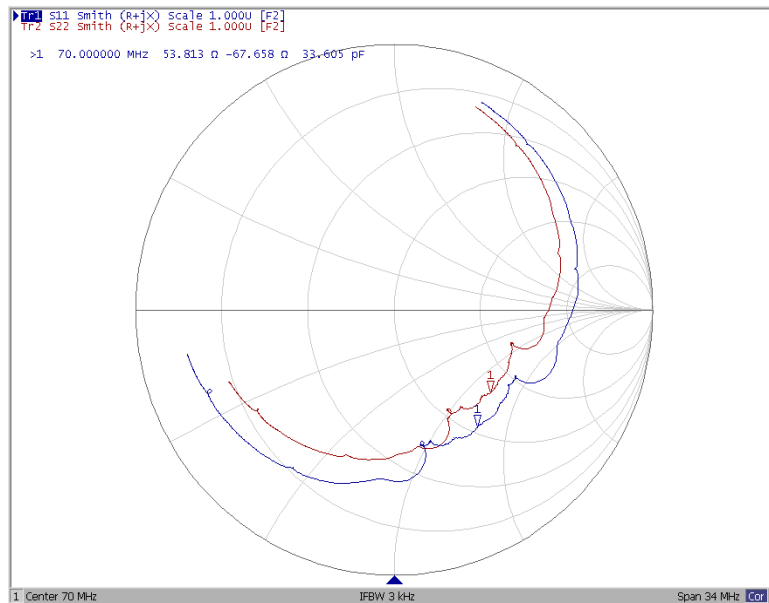
Ripple Variation Fo±3.50MHz



Group Delay Variation Fo±3.50MHz



Smith Chart





VSWR

